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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
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260 BEAR HILL ROAD
WALTHAM, MA 02451-1018

EXAMINER

PHU, PHUONG M

ART UNIT	PAPER NUMBER
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2611

SHORTENED STATUTORY PERIOD OF RESPONSE	MAIL DATE	DELIVERY MODE
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3 MONTHS

02/21/2007

PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

If NO period for reply is specified above, the maximum statutory period will apply and will expire 6 MONTHS from the mailing date of this communication.

Office Action Summary

Application No.

09/788,211

Applicant(s)

JOSEFSSON ET AL.

Examiner

Phuong Phu

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 22 December 2006.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-43 is/are pending in the application.
- 4a) Of the above claim(s) 10-24 and 35-39 is/are withdrawn from consideration.
- 5) ☒ Claim(s) 40-43 is/are allowed.
- 6) ☒ Claim(s) 1,3-6,8,9,25-27,31-34 is/are rejected.
- 7) ☒ Claim(s) 2,7 and 28-30 is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
- ☐ Certified copies of the priority documents have been received.
 - ☐ Certified copies of the priority documents have been received in Application No. _____.
 - ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- ☐ Notice of References Cited (PTO-892)
- ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- ☐ Information Disclosure Statement(s) (PTO/SB/08)
Paper No(s)/Mail Date _____
- ☐ Interview Summary (PTO-413)
Paper No(s)/Mail Date _____
- ☐ Notice of Informal Patent Application
- ☐ Other: _____

DETAILED ACTION

This Office Action is responsive to the Amendment filed on 10/23/06 and the RCE filed on 12/22/06. Accordingly, claims 1-43 are currently pending; and claims 10-24 and 35-39 are withdrawn from consideration.

Double Patenting

1. Claim 32 is objected to under 37 CFR 1.75 as being a substantial duplicate of claim 31. When two claims in an application are duplicates or else are so close in content that they both cover the same thing, despite a slight difference in wording, it is proper after allowing one claim to object to the other as being a substantial duplicate of the allowed claim. See MPEP § 706.03(k).

Claim Rejections - 35 USC § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in a patent granted on an application for patent by another filed in the United States before the invention thereof by the applicant for patent, or on an international application by another who has fulfilled the requirements of paragraphs (1), (2), and (4) of section 371(c) of this title before the invention thereof by the applicant for patent.

The changes made to 35 U.S.C. 102(e) by the American Inventors Protection Act of 1999 (AIPA) and the Intellectual Property and High Technology Technical Amendments Act of 2002 do not apply when the reference is a U.S. patent resulting directly or indirectly from an international application filed before November 29, 2000. Therefore, the prior art date of the reference is determined under 35 U.S.C. 102(e) prior to the amendment by the AIPA (pre-AIPA 35 U.S.C. 102(e)).

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3. Claims 1 and 25-27 are rejected under 35 U.S.C. 102(e) as being anticipated by Raghavan (6,415, 003), previously-cited.

-Regarding to claim 1, see figures 2 and 6, and col. 4, line 46 to col. 7, line 47 and col. 10, lines 20-62, Raghavan discloses a system (see figure 2) comprising:

a transmitter circuit "transmitter" (see col. 4, line 64);

a receiver circuit (200);

a transmission medium (10) having a transfer function ($f(z)$) for transmitting a transmission signal (a_k), in a symbol stream comprising a reference signal and a subsequent transmission signal, between said transmitter and receiver circuits (see col. 4, lines 63-64); and

a calibration circuit (208) responsive to an altered reference signal (r_k) of the transmission signal (a_k) of said transmitter circuit altered by the transmission medium for adjusting the reference signal level (V_{REF}) of the receiver circuits to compensate for variations in the subsequent transmission signal due to said transfer function,

wherein the altered reference signal is the reference signal altered by the transmission medium and received at the receiver; and the reference signal is a start portion of the transmission signal which is used to determine whether the reference signal level (V_{REF}) is adjusted in order to make the transmitter optimally accommodate possible high peak-to-peak voltages of "killer packets" as variations in the subsequent transmission signal due to said transfer function, (see TALBE 1, col. 6, lines 22-32, col. 7, lines 22-25, col. 9, lines 30 to col. 10, line 62). And, a further detailed explanation is following.

In Raghavan, see figure 2, the reference signal level (V_{REF}) is adjusted based on comparing a gain, provided by a gain control circuit (208), with a threshold gain (see col. 10,

lines 50-57). The gain is varied predictably as a function of the cable length of the transmission medium (10) and obtained by comparing a value of sample (a^k), which is derived from the altered reference signal (r_k) (see col. 7, lines 23-25, col. 10, lines 42-63). Further, since the cable length of the transmission medium is fixed and therefore the normalized value of the gain is fixed (see TABLE 1), the adjustment of the reference signal level (V_{REF}) is inherently needed once, at maximum, at the beginning reception of the transmission signal (a_k) in response to a start portion of the transmission signal. For instance, in one of Raghavan's embodiments, the reference signal level is initially set to 2V and if the gain is greater than a threshold gain of 2.5, then the reference signal level is reset to a lower value of 1.33V (see col. 10, lines 50-57).

-Regarding to claim 25, Raghavan discloses a control circuit (comprising (207)) coupled to the transmission medium to synchronize the adjustment of the reference signal level (see figure 2).

-Regarding to claim 26, Raghavan discloses that the control circuit includes a clock circuit (207) (see col. 4, lines 60-61).

-Regarding to claim 27, Raghavan discloses that the control circuit further includes channel control circuit (206, 211, 202, 203, 212, 201, 204, 205) for controlling, at the receiver, compensations on channel distortion, random noise and signal losses cause by the channel function ($f(z)$) (see col. 5, line 8 to col. 7, line 47).

Claim Rejections - 35 USC § 102/103

4. Claims 3 are rejected, under 35 U.S.C. 102(e) as being anticipated by, or under 35 U.S.C. 103(a) as being unpatentable by, Raghavan.

-Regarding to claim 3, Raghavan discloses that said transmission medium are transmission lines such as twisted copper pair, coax cable, etc (see col. 4, lines 64-67). These transmission lines inherently, or obviously by one skilled in the art), has an equivalent circuit model represented by a circuit comprising a capacitor circuit "isolation barrier circuit".

Claim Rejections - 35 USC § 103

5. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

6. Claims 4, 6, 8 and 31-34 are rejected under 35 U.S.C. 103(a) as being unpatentable over Raghavan in view of Hoekstra (5,883,907) (previously-cited).

-Regarding to claim 4, Raghavan discloses that said receiver circuit includes an analog to digital circuit (203) having an analog input coupled to the transmission medium for receiving an analog symbol stream communicated across the transmission medium and providing a digital output signal (x_k) (see figure 2).

Raghavan does not disclose said transmitter circuit includes a digital to analog circuit having an analog output coupled to said transmission medium and an input for receiving an digital input signal to be communicated across said transmission medium.

Hoekstra discloses a transmitter (12) (see figure 1) for transmitting data symbol stream across a transmission medium (34) to a receiver (14) at the other end wherein the transmitter comprises a digital to analog circuit (D/A) (see figure 2) having an analog output coupled to said transmission medium and an input for receiving a digital data symbol stream (outputted from (80)) to convert said digital data symbol stream into an analog data symbol stream to be communicated across said transmission medium (see figures 1 and 2, and col. 2, line 31 to col. 3, line 67).

Since Raghavan does not disclose in detail how the transmitter circuit is implemented, therefore, for an application of implementing said Raghavan transmitter circuit, it would have been obvious for one skilled in the art, to implement Raghavan transmitter circuit in such a way that the transmitter circuit would comprise a digital to analog circuit (D/A) having an analog output coupled to said transmission medium and an input for receiving a digital data symbol stream to convert said digital data symbol stream into an analog data symbol stream to be communicated across said transmission medium to be received by the receiver circuit, as taught by Hoekstra, without affecting the overall system performance.

-Regarding to claim 6, Raghavan in view of Hoekstra teaches that said transmitter comprises an encoder circuit (80) responsive to a digital input signal to provide said digital data symbol stream and a digital to analog converter ((D/A) responsive to said digital data symbol stream signal to provide to said transmission medium an analog data symbol stream signal (see Hoekstra, figure 2).

-Regarding to claim 8, as applied to claims 4 and 6, Raghavan in view of Hoekstra discloses that said analog to digital circuit includes an analog to digital converter (203) (see

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Raghavan, figure 2) responsive to a analog input (r_k) from the transmission medium to provide a digital signal (x_k). Raghavan in view of Hoekstra further teaches that the receiver circuit could be implemented to include a decoder circuit (82) responsive to said digital signal to provide a digital output signal (see Hoekstra, figure 2).

-Regarding to claims 31 and 32, Raghavan in view of Hoekstra discloses said analog output and said analog input can be constant average signals (NRZ, MLT3, etc) having constant averages (see Raghavan, col. 1, lines 31-62).

-Regarding to claim 33, Raghavan discloses that said calibration circuit includes a reference signal capture circuit (104) for capturing an altered reference signal and providing said altered reference signal to said receiver circuit, wherein said altered reference signal compensates for variations in the transmission signal due to said transfer function (see Raghavan, figure 1A).

-Regarding to claim 34, Raghavan discloses that said calibration circuit includes a reference signal averaging circuit (202) connected to said reference signal capture circuit for averaging "smoothing" by "noise filtering" said altered reference signal and providing an averaged altered reference signal to said receiver circuit (see figure 2, and col. 5, line 65 to col. 6, line 2).

7. Claims 5 and 9 are rejected under 35 U.S.C. 103(a) as being unpatentable over Raghavan in view of Hoekstra, and further in view by Salinger (prior art of record).

-Regarding to claims 5 and 9, Raghavan in view of Hoekstra discloses that said digital to analog circuit includes a digital to analog converter (D/A) with an input for receiving a digital input signal (see Hoekstra, figure 2), and said analog to digital circuit includes an analog to

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digital converter (203) (see Raghavan) responsive to a analog signal to provide a digital output signal.

Raghavan in view of Hoekstra does not disclose a modulation circuit responsive to said digital to analog converter for providing an analog output to be transmitted on the transmission medium and a demodulator circuit responsive to an analog input received from the transmission medium to provide said analog signal to said analog to digital converter.

Salinger teaches using a modulation circuit (24), at a transmitter (TRANSMITTER), to up-convert a signal (to be transmitted on a transmission channel (30) to a receiver (RECEIVER) at a receiving end) to the transmission frequency bandwidth of the transmission channel, being required by his system, and a demodulation (36) at said receiver to down-covert said received up-converted signal for recovering said signal (see figure 2, and col. 5, lines 32-67).

It would have been obvious for one skilled in the art, when building or carrying out Raghavan invention in view of Hoekstra, upon a system's requirement on its transmission frequency bandwidth of a transmission channel, to implement Raghavan invention in view of Hoekstra with a modulation and demodulation, as taught by Salinger, in such a way that the transmitter circuit would comprise a modulation to up-convert the analog output signal outputted from the digital to analog converter to the required bandwidth of the transmission channel for the transmission on the transmission medium, and the receiver circuit would comprises a demodulator for receiving and down-converting said up-converted signal to the analog signal to be provided to the analog to digital converter, in order to meet the system's requirement on its transmission frequency bandwidth.

Allowable Subject Matter

8. Claims 2, 7 and 28-30 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.
9. Claims 40-43 are allowed.

Response to Arguments

10. Applicant's arguments filed on 10/23/06 have been fully considered but they are not persuasive.

The applicant mainly argues that Raghavan does not disclose a reference signal and a transmission signal as claimed.

The examiner respectfully disagrees. As being explained in claim 1 set forth above in this Office Action, Raghavan discloses the reference signal "reference signal" and the transmission signal "subsequent transmission signal", as claimed.

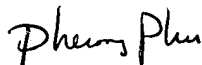
Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Phuong Phu whose telephone number is 571-272-3009. The examiner can normally be reached on M-F (8:00 AM - 4:30 PM).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Jay Patel can be reached on 571-272-2988. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.



PHUONG PHU
PRIMARY EXAMINER

Phuong Phu
Primary Examiner
Art Unit 2611

Phuong Phu
02/08/07